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<u>REMARKS</u>

Reconsideration of the application in light of the amendments and the following remarks is respectfully requested.

Status of the Claims

Claims 1-14 are pending. Claims 1, 4, 9, 10, 13 and 14 have been amended. Support for the amendments can be found in the Specification on page 6, lines 16-22. No new matter has been added.

Claim Objections

The Examiner has objected to claims 9, 10, 13 and 14 under 37 C.F.R. 1.75(a) for containing informalities. Applicant has amended claims 9, 10, 13 and 14 and submits that the claims are in conformance with 37 C.F.R. 1.75(a). Reconsideration and withdrawal of the objection is requested.

Rejection Under 35 U.S.C. § 102

Claims 1-14 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 4,801,859 to Dishner. The Examiner contends that Dishner discloses all of the features recited in the claims. Applicant respectfully traverse the rejection.

Claim 1 has been amended to recite "a duty ratio setting means comprising an A/D converter, said duty ratio setting means performing proportion, integration and differentiation

operations to calculate a control value for the switching element and setting a duty ratio corresponding to said control value so that deviation between said target booster voltage and said actual booster voltage is eliminated." Independent claim 4 has been amended to recite "performing proportion, integration and differentiation operations to calculate a control value for the switching element" and "setting a duty ratio corresponding to said control value to eliminate a deviation between said target booster voltage and said actual booster voltage."

In contrast, Dishner does not disclose or suggest calculating a control value and setting the duty ratio in accordance with the calculated control value. Rather, Dishner recites "The integrated circuit 90 develops a pulse width modulated (PWM) waveform at a pin 9 having a duty cycle determined in accordance with the magnitude of the error signal developed by the error amplifier 92. The PWN waveform is coupled through a resistor to the base of the transistor Q1. If necessary or desirable, the output appearing at the pin 9 may be amplified before it is applied to the base of the transistor Q1." (emphasis added). See Dishner, column 7, lines 39-45. Furthermore, Dishner discloses controlling the switching element "by means of a level comparator 96 which compares the speed signal developed by the speed sensor 94 against a reference signal S_{REF} representing the speed S₁." See Dishner, column 7, lines 55-57.

Therefore, Dishner does not disclose or suggest "performing proportion, integration, and differentiation operations to calculate a control value for the switching element" and "setting a duty ratio corresponding to said control value" as recited in amended claims 1 and 4. Accordingly, Applicant submits that Dishner does not disclose each and every feature recited in claims 1 and 4, and respectfully requests withdrawal of the above rejections.

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Claims 2, 3, 7, 8, 11 and 12 depend from claim 1. Claims 5, 6, 9, 10, 13 and 14 depend

from claim 4. Applicant submits that claims 2-3 and 5-14 are patentable over Dishner for at least

the same reasons their respective base claim. Applicant respectfully requests withdrawal and

reconsideration of the rejection.

CONCLUSION

Each and every point raised in the Office Action dated May 26, 2005 has been addressed

on the basis of the above amendments and remarks. In view of the foregoing it is believed that

claims 1-14 are in condition for allowance and it is respectfully requested that the application be

reconsidered and that all pending claims be allowed and the case passed to issue.

If there are any other issues remaining which the Examiner believes could be resolved

through a Supplemental Response or an Examiner's Amendment, the Examiner is respectfully

requested to contact the undersigned at the telephone number indicated below.

Dated: November 21, 2005

Respectfully submitted,

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